

Tim A. Schreyer
B2B Content And Copy Writer

TAS Writing Services
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Let's Get Your Business Noticed!



You solve important problems for your clients. But how do you put that into words? How do you say it without bragging – without sounding like a lot of hype?

I help small businesses attract new clients and close more sales, by using true-story case studies, eBooks, and white papers to build awareness, familiarity, social proof, and above all, trust in you and your business.

I especially love using the “power of story” to take your customer feedback to the next level. The way I think of it is that a testimonial is just an essay, but a story is a picture.

Testimonials tell. Stories sell!

I am available to help you, working either as a regular employee or as a freelancer through my company, TAS Writing Services. My background is in electrical engineering and computer programming, and I can help you produce a wide variety of B2B and technical content. In addition to the types of content listed above, I can also help you produce user manuals, technical reports, blog posts, and more.

I'd love to help you grow your business! Let's schedule a short discovery chat!

Education

Copywriting Courses, 2000-present

Including many classes from AWAI, Mike Shreve, Ed Gandia, Gordon Graham, Jon Morrow, Udemy, and others

Ph. D. in Electrical Engineering, Stanford University, 1989

Dissertation: *The Effects of Interconnection Parasitics on VLSI Circuit Performance*, applying transmission line theory (Time-domain slow-wave model) to metal traces on integrated circuits.

Employment

Owner, TAS Writing Services, Chromo, Colorado, 2017-Present
B2B copywriting services

Owner, Mr. Tim's Workshop, Chromo, Colorado, 2012-Present
Software development, eCommerce, marketing, and IT services.

High School Teacher, Immanuel Lutheran School, Mankato, Minnesota, 2007
Taught Physical Science, Advanced Math, Geometry, Chemistry, and Computer Programming.

Owner, System Design Concepts, Inc., Chromo, Colorado, 2001-2012
Website development, marketing, and IT services.

Chairman and Founding Member, Upper San Juan Search And Rescue, Inc., 2005-2007.
Authored By-Laws and directed this not-for-profit corporation providing volunteer emergency assistance for Archuleta County, Colorado.

Executive Board Secretary and Founding Member, Colorado Mounted Rescue, Inc., 2003 – 2004.
Authored By-Laws and helped operate this volunteer organization in Archuleta County, Colorado.

Sr. Staff Engineer, Intel Corporation, Hillsboro, Oregon, 1989-2002
Signal Integrity strategist in Intel's system design group.
Created, documented, and taught system design techniques for use by Intel's customers.

Additional Abilities

Experienced in many computer and web programming languages (C#, Java, Python, PHP, and others)

Content And Copywriting Portfolio



5 Reasons Why API Security and Engineering Teams Must Work Together

Hackers are becoming more creative, and the strategies to protect against them are becoming more complicated. Security must be implemented at the code level during API development, but this places a burden on software engineers.

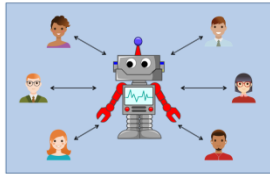
[Read It Here](#)



Security Observability: Why Tracing?

Cyber-attacks cost organizations as much as \$13 million per year, but detecting these attacks can take as long as 200 days. What if we could reduce that time to zero? Tracing is the key to detecting attacks as they happen.

[Read It Here](#)



Chatbot Software in 2020: Choosing the Best Bot to Attract Customers to Your Business

This is a spec assignment for the Content Marketing Certification class from Jon Morrow at SmartBlogger.com.

[Read It Here](#)



Technical Writing: pbnArtist User's Manual

I am also a computer programmer, and I have written a program called pbnArtist. This is the user's manual for that software.

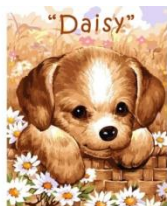
[Read It Here](#)



High Meadows Mortgage Website

This website, which I designed and wrote for High Meadows Mortgage, takes its reader through the phases of Attention, Interest, Desire, Proof and Action (AIDPA) by using the following elements: ...

[Read It Here](#)



Facebook Ads

Here are three partial page ads. The first two are for Facebook, and the third one is a spec assignment (not a real product) for an AWAI B2B email class.

[Read It Here](#)

Other Publications and Documents

“**Upper San Juan Search And Rescue By-Laws**”, Upper San Juan Search & Rescue Inc., Pagosa Springs, Colorado, August 2005.

“**Colorado Mounted Rescue By-Laws**”, Colorado Mounted Rescue Inc., Pagosa Springs, CO, June 2003.

Tim Schreyer, “**High Speed Digital Interconnect Design**” and “**High Speed Digital Interconnect Theory**” (16 hr. Class and Student Handouts), Intel Corporation internal publication, later licensed to System Design Concepts, Inc., August 1999.

R. Martin, X. Moua and T. Schreyer, “**Klamath (PentiumPro™ Processor) Frontside Bus and Clock Layout Design Guide**”, Intel Corporation, 1996.

Tim Schreyer, “**High Speed Signal Modeling**” (8 hr. Class and Student Handouts), Intel Corporation internal publication, January 1990.

Tim Schreyer, several internal design specifications and design/validation reports, Intel Corporation internal documents, 1989-2002.

Timothy Alan Schreyer, “**The Effects of Interconnection Parasitics on VLSI Circuit Performance**”, Ph. D. Dissertation, Stanford University, March 1989.

T. A. Schreyer, Y. Nishi, and K. C. Saraswat, “**Simulation and Measurement of Picosecond Step Responses in VLSI Interconnections**”, *1988 IEDM Tech. Digest*, pp. 344-347.

T. A. Schreyer, Y. Nishi, and K. C. Saraswat, “**A Complete RLC Transmission Line Model of Interconnect Delay**”, *Proc. Symposium on VLSI Technology* (San Diego, CA), May 1988, pp. 95-96.

T. A. Schreyer, A. J. Bariya, J. P. McVittie, and K. C. Saraswat, “**Specific Contact Resistivity Measurements of RIE Etched Contacts**”, *Journal of Vacuum Science and Technology A*, vol. 6, no. 3, May/June 1988, pp. 1402-1406.

T. A. Schreyer, P. J. Wright, and K. C. Saraswat, “**The Effect of a Superconducting Interconnect on Circuit Performance**”, *Proc. IEEE Dev. Research Conference* (Santa Barbara, CA), June 1987, p. VIB-8.

W. Loh, S. Swirhun, T. Schreyer, R. Swanson, and K. Saraswat, “**Modeling and Measurement of Contact Resistances**”, *IEEE Trans. Electron Devices*, vol. ED-32, no. 3, March 1987, pp. 512-524.

T. A. Schreyer, and K. C. Saraswat, “**A Two-Dimensional Analytical Model of the Cross-Bridge Kelvin Resistor**”, *IEEE Electron Device Letters*, vol. EDL-7, no. 12, Dec. 1986, pp. 661-663.

W. M. Loh, P. J. Wright, T. A. Schreyer, S. E. Swirhun, K. C. Saraswat, and J. D. Meindl, “**The Sidewall Resistor - A Novel Test Structure to Reliably Extract Contact Resistivity**”, *Proc. IEEE Device Research Conference* (Amherst, MA), June 1986, also published in *IEEE Electron Device Letters*, vol. EDL-7, no. 8, Aug. 1986, pp. 477-479.

K. Saraswat, W. Loh, T. Schreyer, and S. Swirhun, “**Measurement and Extraction of Specific Contact Resistivity**”, *IEEE VLSI Multilevel Interconnect Conference* (Santa Clara, CA), June 1986, pp. 385-391.

T. Schreyer, S. Swirhun, W. Loh, K. Saraswat, and R. Swanson, “**Comparison of Test Structures Used for the Measurement of Low Resistive Metal-Semiconductor Contacts**”, *Proc. IEEE Workshop on Test Structures* (Long Beach, CA), Feb. 1986, pp. 8-23.

W. M. Loh, S. E. Swirhun, T. A. Schreyer, K. C. Saraswat, and R. M. Swanson, “**Two-Dimensional Simulations for Accurate Extraction of the Specific Contact Resistivity from Contact Resistance Data**”, *1985 IEDM Tech. Digest*, pp. 586-589.